

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	2712675
<b>Application Number:</b>	10677596
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	8230
<b>Title of Invention:</b>	Integrated power supply circuit for simplified board design
<b>First Named Inventor/Applicant Name:</b>	William R. Eisenstadt
<b>Customer Number:</b>	30448
<b>Filer:</b>	Eduardo Jose Quinones/Juliana White
<b>Filer Authorized By:</b>	Eduardo Jose Quinones
<b>Attorney Docket Number:</b>	5853-268
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<b>Filing Date:</b>	02-OCT-2003
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<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		5853268.pdf	1194365	yes	13
			32e81f78c78a1ca4ea551833cd27dded7a728087		

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Amendment After Final	1	1
	Claims	2	7
	Applicant Arguments/Remarks Made in an Amendment	8	13
Warnings:			
Information:			
Total Files Size (in bytes):		1194365	
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